EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1536	disabl$5 adj clock$1	USPAT	OR	OFF	2006/01/26 14:00
S2	40	disabl$5 adj clock$1 near5 processor$1	USPAT	OR	OFF	2006/01/26 14:25
S3	3	disabl$5 adj clock$1 near5 processors	USPAT	OR	OFF	2006/01/26 14:25
S4	4	disabl$5 adj clock$1 with processors	USPAT	OR	OFF	2006/01/26 14:26
S5	0	disabl$5 adj clock$1 near10 processor$1 same (integer)	USPAT	OR	OFF	2006/01/26 14:27
S6	11	disabl$5 adj clock$1 near10 processor$1 and (integer)	USPAT	OR	OFF	2006/01/26 14:27
S7	4	disabl$5 adj clock$1 near10 processor$1 and thread$1	USPAT	OR	OFF	2006/01/26 14:28

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	10	disabl$5 adj clock$1 near10 processor$1 and thread$1	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/26 15:51
L3	18	disabl$5 adj clock$1 near10 processor$1 and (integer)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/01/26 15:51
S1	1536	disabl$5 adj clock$1	USPAT	OR	OFF	2006/01/26 14:00
S2	40	disabl$5 adj clock$1 near5 processor$1	USPAT	OR	OFF	2006/01/26 14:25
S3	3	disabl$5 adj clock$1 near5 processors	USPAT	OR	OFF	2006/01/26 14:25
S4	4	disabl$5 adj clock$1 with processors	USPAT	OR	OFF	2006/01/26 14:26
S5	0	disabl$5 adj clock$1 near10 processor$1 same (integer)	USPAT	OR	OFF	2006/01/26 14:27
S6	11	disabl$5 adj clock$1 near10 processor$1 and (integer)	USPAT	OR	OFF	2006/01/26 15:51
S7	4	disabl$5 adj clock$1 near10 processor$1 and thread$1	USPAT	OR	OFF	2006/01/26 15:51